# **RESUME**

# Dr. M B Anandaraju

Professor

Dept. of Electronics & Communication Engineering

B.G.S Institute of Technology B G Nagar, Bellur cross, NH-48

Nagamangala(T), Mandya(D)-571448

(Karnataka)

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### **PERSONAL PROFILE:**

Date of Birth: 23-06-1973.

Date of Joining: 03/07/2006( to BGSIT)

Type of Association: Regular Designation: Professor, BGSIT

Languages Known: Kannada and English.

### **EDUCATION AND CERTIFICATION:**

QUALIFICATION	SPECIALIZATION	INSTITUTION	YEAR
Ph.D.	Electronics	University of Mysore	2014
M.E.	Power Electronics	UVCE, Bangalore University	2003
B.E.	Electrical and Electronics	PESCE, University of Mysore	1996

#### **PROFESSIONAL ACTIVITIES**

Presently working as Professor in the Department of ECE, BGSIT and actively involved in lecturing, administration and organizing several workshops and technical talks for faculty members and engineering students and also involved in research works.

#### WORK EXPERIENCE

SI. NO.	UNIVERSITY/COLLEGE	DESIGNATION	DURATION	PERIOD
1	BGSIT, BG Nagar	Professor & HOD	09 years	2008 to 2020(oct)

2	BGSIT, BG Nagar	Asst. Professor & HOD	02 years	2006 to 2008
3	JVIT, Bidadi	Asst. Professor	03 years	2003 to 2006

4	Shirdi Sai Engineering College, Bangalore	Lecturer	03 years	2000 to 2003
5	Oxford Polytechnic, Bangalore	Lecturer	02 years	1996 to 2000

TRAINING PROGRAMME/SEMINARS/CONFERENCES CONDUCTED:

Sl.	PROGRAM NAME
NO.	
1	Two days Faculty Development Program on "Recent Trends in CMOS VLSI Design & Embedded Systems" at BGSIT, BG Nagar on 24 <sup>th</sup> February 2011-25 February 2011.
2	Five days faculty development program on "Advances in Communication & Network systems" at BGSIT on 12 <sup>th</sup> March 2012 to 16 <sup>th</sup> March 2012.
3	Technical Talk on "Networking in Communication Systems" at BGSIT on 10 <sup>th</sup> sep - 2012
4	First National Conference on "Emerging Trends in Electronics and Communication" (NCETEC-2014) at BGSIT on 4 <sup>th</sup> April-2014.
5	Second National Conference on "Emerging Trends in Electronics and Communication" (NCETEC-2015) at BGSIT on 30 <sup>th</sup> April-2015.
6	Third National Conference on "Emerging Trends in Electronics and Communication" (NCETEC-2015) at BGSIT on 5 <sup>th</sup> May-2016.

## TRAINING PROGRAMME/SEMINARS/CONFERENCES ATTENDED:

SL.	PROGRAM NAME		
1	One-Day Workshop on "New Paradigms in the Accreditation System" at SJBIT, Bangalore on 04 <sup>th</sup> November, 2011.		
2	VTU-VGST's FDP on "Recent Advances in Medical Imaging Techniques" at GSSSIET for Women, Mysore on 26 <sup>th</sup> to 30 <sup>th</sup> March, 2012.		

# **INTERNATIONAL JOURNAL PUBLICATIONS**

- M.B.Anandaraju, P.S.Puttaswamy, "Modified Interactive Evolutionary Computing for Speed Control of an Electric DC Motor" pp.19-24, IJCA, Volume-39, No. 15, February 2012.
- <sup>2</sup> M.B.Anandaraju, P.S.Puttaswamy, "Genetic Algorithm: An approach to Velocity

  Control of an Electric DC Motor" pp.37-43, IJCA, Volume-26, No. 1, July 2011.
- 3. M.B.Anandaraju, P.S.Puttaswamy, "Comparative Studies on Evolutionary Computational Techniques for Speed Control of A DC Motor", pp 11525-11539, IJAREEIE, Volume-3, issue-8, August 2014.
- <sup>4</sup> M B Anandaraju, Rashmi H, Naveen K B, "Design and Synthesis of binary LDPC Decoder", pp. 1015-1017, IJTRIE, ISSN: 2347-4718, Volume-1, Issue 9, May 2014.
- <sup>5</sup> M B Anandaraju, Naveen Pai G, Naveen K B, "Design and Synthesis of lower bus Bus Invert Encoding and Decoding Technique Using Reversible Logic", pp. 143-146, IJERA, ISSN: 2248-9622, Volume-3, Issue-4, Jul-Aug 2013.
- 6 M.B.Anandaraju, Yukthi.B.R, Savitha.A.P, Nuthan.A.C, "FPGA Based Implementation of Image Encryption Using Scan Patterns and Carrier Images" pp.45-47, IJISME, ISSN: 2319-6386, Volume-1, Issue-7, June 2013.
- M.B.Anandaraju, Suvarna.M, Prabhavathi.K, Nuthan.A.C, "Design and Implementation of Highly Secure Cryptosystem for Image Encryption" pp.28-31, IJIES, ISSN: 2319-9598, Volume-1, Issue-7, June 2013.

- 8 M.B.Anandaraju, Shalini.M.G, Punithkumar.M.B, "FPGA-Based Implementation of Intelligent Predictor using ANN for Global Solar Irradiation" pp.111-117, IJITEE, ISSN: 2278-3075, Volume-3, Issue-2, July 2013.
- 9 M.B.Anandaraju, Prashanth.N.G, Savitha.A.P, Nuthan.A.C "Design and Synthesis of Fault Tolerant Full Adder / Subtractor Using Reversible Logic Gates" pp.137-142, IJERA, ISSN: 2248-9622, Vol. 3, Issue 4, Jul-Aug 2013.
  M. B.
- Anandaraju, Niveditha. H. R, Savitha. A. P, "Fast And Reconfigurable PCE In FPGA-Based Firewall", pp.3035-3041, IJERT, ISSN: 2278-0181, Vol.2 Issue 6, June 2013.
- M.B. Anandaraju, MD. Belal Rashid, Balaji B.S, "VLSI Design and Implementation of Binary NumberMultiplier based on Urdhva Tiryagbhyam Sutra with reduced Delay and Area" pp.269-278, IJERT, ISSN: 0974-3154, Volume 6, Number 2, 2013.
- Anandaraju. M. B, Kokila. K. S, "Physical Layer Implementation of OFDM (Orthogonal Frequency Division Multiplexing) For Software Defined Radio On FPGA", pp.2505-2507, IJERT, ISSN: 2278-0181, Vol. 2 Issue 6, June 2013.
- 13 M.B.Anandaraju, Kartheek.B.V, Manojkumar.S.B, "Design and Implementation of Modified Adaptive Filtering Algorithm for Noise Cancellation in Speech Signal on FPGA for Minimum Resource Usage" pp.18-21, IJIES, ISSN: 2319–9598, Volume-1, Issue-7, June 2013.
- 14 M.B.Anandaraju, Dr. Sunitha S L"ECG Screening in Attention Deficit Hyperactivity Disorder" pp. 247-248, IJIET, ISSN:2319-1058, Volume 7 Issue 3 October 2016
- 15 M.B.Anandaraju, Yogananda C D, Naveen K B "Design of Convolutional encoder using 16 bit reversible logic vedic multiplier" pp. 19-29, IJAER, Vol. No. 12, Issue No. II, August.
- 16 Dr. M B Anandaraju, Mr. Naveen K B, Rajani "Monitoring of Ozone in ambient air and controlling of Oxygen Generated with RTC" pp. 799-805, IJSRR, ISSN: 2279-543X, Volume 7, Issue 6, June 2018.
- 17 **Dr. M B Anandaraju, Sumitha C, Nuthan A C "Phase retrieval imaging for nanoscale images using MATLAB"** pp. 2492-2497, IJRECE, ISSN: 2393-9028, Volume. 7 Issue 1, March 2019.
- 18 R.M B Anandaraju, C Sumitha "MATLAB/Simulink based Bouc-Wen model for Nano-Positioning System" 2021 International Conference on Intelligent Technologies (CONIT).
  - 19 Dr. M BAnandaraju, Sneha M P, Vandana H S, Varshitha C K, Varshin H K

- "Advanced Robot for Automatic waste segregation and status alert" pp 3824-3826, IJRASET,ISSN: 2321-9653 Volume 7, Issue V, May 2019.
- 20 Dr. M B Anandaraju, Brunda G J, Deepthi H Y, Lohith M, Lokananda P, "IOT based blood booking for hospitals and remote elderly monitoring system" pp-1473-1478, IJRASET ISSN: 2321-9653 Volume 7, Issue V, May 2019.
- 21 Dr. M B Anandaraju, Manoj Kumar S B, ShamaFirdose U, Dr. H S Sheshadri "Retinal Disease Detection Using LBP Technique" pp 3433-3439, IJRAT, ISSN: 2321-9637, Vol.6, No.12, Dec-2018.
- 22 **Dr M B Anandaraju, "Accident avoiding system using the IOT"** Journal of Emerging Technologies and Innovative Research (JETIR), ISSN: 2349-5162, Vol. 6, Issue 5, May-201.9
- 23 Dr. M B Anadaraju, "Phase retrieval imaging for nano-scale images using MATLAB" International journal of research in electronics and computer engineering, ISSN: 2348-2281, Vol.7, Issue 1, Jan-March 2019
- 24 **Dr. M B Anandaraju, "Statistical Analysis of Attention Deficit Hyper Activity Disorder"** Journal of Huazhong University of Science and Technology, ISSN:1671-4512, Volume-50, Issue 02, Feb 2021.

### **AREAS OF INTEREST:**

- · Power Electronics
- · Control System
- · Analog electronic circuits
- · Digital Signal Processing
- · Biomedical Signal Processing
- · Microprocessor
- · Network Analysis
- · Neural Networks

#### AREAS IN WHICH EXPERT LECTURERS CAN BE DELIVERED:

- · Power Electronics
- · DC and AC Motors
- · Network Analysis
- · Circuit theory
- · Control System

- · Digital Signal Processing
- · Microprocessor
- · Neural Networks

## CONTRIBUTION TO THE DEPARTMENT / COLLEGE:

· Director for Training and Placements (BGS Group of Technical Institutions). ·

Member of Placement Committee.

- · Member of Administration Council.
- · College Admission Incharge.
- · Member of Sports Committee.
- · Member of Stock Verification Committee.
- · Member of Anti-Ragging Committee.
- · Academic Incharge.

#### **SPONSORED PROJECT UNDERTAKEN:**

Sl. No.	Sponsoring Agencies	Resear ch Center	Title	Amount Sanctioned/Year	Status
01	VGST, Bangalore (Under TRIP Program)	BGSIT, BG Nagar	Sleep Apnea Analysis Using EEG Signals	40 Thousand/ 2012-13	Completed

### RECOGNIZED PhD GUIDE IN THE FOLLOWING

**UNIVERSITIES:** ➤ Visvesvaraya Technological University

➤ University of Mysore

# PLACE: BG Nagar [Prof. M B Anandaraju] DATE: